

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : **03-201882**

(43)Date of publication of application : **03.09.1991**

(51)Int.Cl.

H04N 5/278

G09G 5/24

H04N 5/445

H04N 5/66

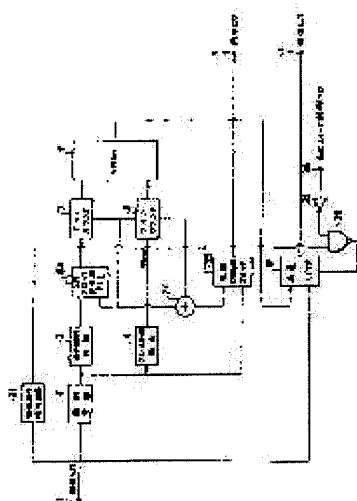
(21)Application number : **01-344273**

(71)Applicant : **TOKYO ELECTRIC  
POWER CO INC:THE  
SONY CORP**

(22)Date of filing : **28.12.1989**

(72)Inventor : **KIMURA HIROSHIGE  
WATANABE TAKEMOTO  
SHIMIZU HIDEO  
IKEDA YASUNARI  
KATSUMATA TORU**

## (54) VIDEO DISPLAY CIRCUIT



(57)Abstract:

PURPOSE: To attain stable and easy to see display of a video signal from a storage element and convenience of use by generating an internal synchronizing signal based on a readout clock when no external video signal is inputted, supplying the generated synchronizing signal to a monitor, reading a video signal from the storage element and supplying the signal to the monitor.

CONSTITUTION: A detection circuit 21 detects the presence of an external video signal fed externally. In the absence of the external video signal, an internal synchronizing signal is generated based on a readout clock generated by clock generating means 6A, 7, 8 and fed to a monitor, and a video signal is read from a storage element 9 and the only the read video signal is fed to the monitor, Where the signal is displayed. That is, the external video signal is deleted and only the video signal from the storage element 9

is displayed as if it is a character written on a blackboard.